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<b>To:</b>	<b>Examiner Cuong Q. Nguyen</b> <b>GROUP ART UNIT: 2811</b>	<b>From:</b>	Steven Fischman
<b>Fax:</b>	(703)-872-9306	<b>Pages:</b>	5 (including fax cover sheet)
<b>Phone:</b>		<b>Date:</b>	February 24, 2004
<b>Re:</b>	<b>Douglas D. Coolbaugh, et al.</b> <b>U.S. Serial No. 09/516,615</b> <b>Docket: BUR919990190US1 (13020)</b>	<b>CC:</b>	

**COMMENTS:**

Transmitted herewith are the following:

1. Combined Notice of Appeal From the Primary Examiner to the Board of Patent Appeals and Interferences & Petition for Extension of Time Under 37 C.F.R. 1.136(a) (in duplicate)
2. Authorization to charge IBM Deposit Acct. No. 50-0510/IBM (Burlington) in the amounts of \$330.00 Fee for Notice of Appeal & \$420.00 Fee for Extension of Time (2 months)
3. Certificate of Transmission by Facsimile, dated February 24, 2004 (in duplicate)

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**COMBINED NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE  
BOARD OF PATENT APPEALS AND INTERFERENCES & PETITION FOR  
EXTENSION OF TIME UNDER 37 C.F.R. 1.136(a) (Large Entity)**Docket No.  
**BUR919990190US1 (13020)**In Re Application Of: **Douglas D. Coolbaugh, et al.**Serial No.  
**09/516,615**Filing Date  
**March 1, 2000**Examiner  
**Cuong Q. Nguyen**Group Art Unit  
**2811**Invention: **METHOD OF FABRICATING A POLYSILICON CAPACITOR UTILIZING FET  
AND BIPOLAR BASE POLYSILICON LAYERS****TO THE COMMISSIONER FOR PATENTS:**

This combined Notice of Appeal from the Primary Examiner to the Board of Patent Appeals and Interferences and petition for extension of time under 37 CFR 1.136(a) is respectfully submitted by the undersigned:

  
SignatureDated: February 24, 2004

Steven Fischman  
Registration No. 34,594  
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